

Press Release

LDO-Voltage-Regulator-Series S-1137 with Soft-Start

Prominent property of this new Series of CMOS voltage regulator is a Soft-Start function settable by one external capacitor. This will eliminate dangerous inrush current of approximately 400mA and reduce it to uncritical 20mA. The High-Ripple-Rejection-Ratio of 70dB typ. at 1KHz and $V_{OUT} = 1.0V$ is further insuring that noticeable input voltage transients, as they occur with the fluctuation of neighbouring loads, will be suppressed.

Another special feature is the availability of an optional output shunt that will effectuate a fast discharge of output voltage that might be stored on external Capacitors when the regulator is switched off using the ON/OFF control input. Switching the regulator off has another advantage in the extension of battery life time.

A built-in overcurrent protection circuit avoids damage of the output transistor by limiting the output current.

Low-Drop-Out voltage of 210mV is given at an output current of 300mA and $V_{OUT} = 2.8V$. The regulators current requirement for this condition is 45 μ A typ. and 65 μ A max. , in shutdown mode this reduces to 0.1 μ A typ. and 1.0 μ A max. .

Packages may be chosen from SOT-89-5, SOT23-5 and SNT-6A(H), they permit a dense layout.

Capacitors with low equivalent series

resistance (ESR) e.g. the very common ceramic capacitors can be used for phase compensation. Input voltage range is 1.7V to 5.5V. Output voltages are offered in 0.05V steps from 1.2V to 3.5V with an accuracy of $\pm 1.0\%$. Below 1.45V an accuracy of $\pm 15mV$ is achievable.

Datasheets: http://datasheet.sii-ic.com/en/voltage_regulator/S1137_E.pdf

Samples: may be ordered from Seiko Instruments GmbH, Siemensstraße 9, D-63263 Neu-Isenburg.

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